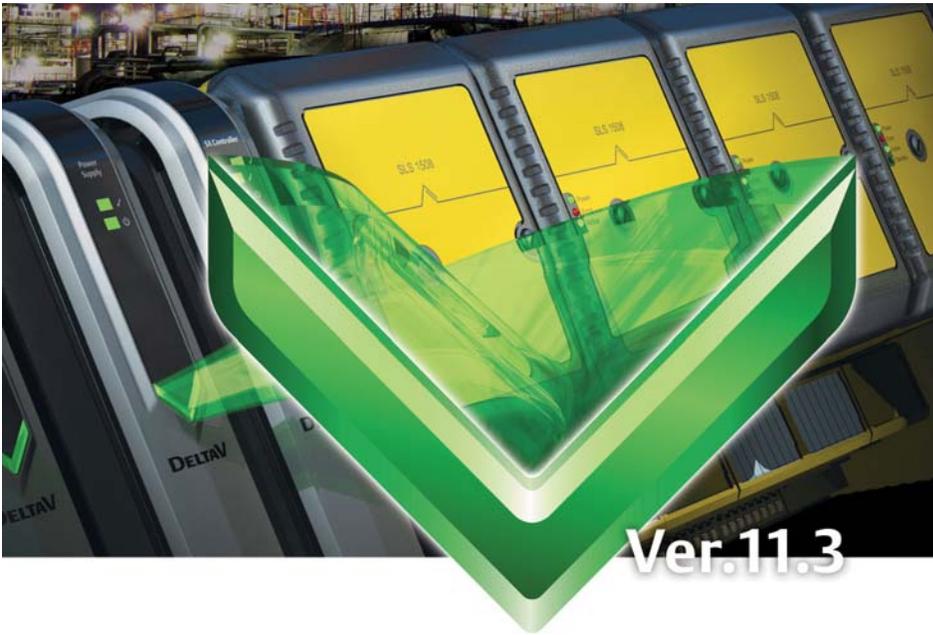


DeltaV SIS™ v11.3 Release—Increased Scalability and Flexibility



update speeds. This meets the needs of most PID process control applications such as flow, pressure, level, and temperature.

Smart SIS

Expanded HART Support

The DeltaV SIS system now works with HART v7 to help with regulatory compliance. With HART v7 and the DeltaV SIS system, software write protection prevents handheld configuration but allows changes from AMS Device Manager where security is enforced.

Built for IEC 61511 Compliance

Protected Composite Templates

For reduced complexity in global engineering and IEC 61508 compliance, the DeltaV SIS v11 system includes protected composite templates. Templates, such as certified safety logic, are created on a 'master' DeltaV SIS system and then distributed to engineering centers for implementation. The templates are protected from modification by only allowing changes to occur on the master system.

Modular Distributed Architecture

SISNet Domains

Now safety applications can be logically separated by process areas, units or applications into SISNet Domains. The SISNet Domains provide design flexibility, support for large projects, easier expansion and easier isolation of separate SIS applications.

Increased Scalability for Large Projects

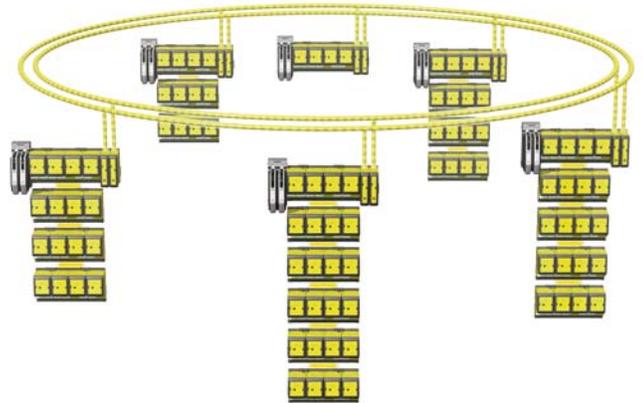
The only remaining capacity limits for the DeltaV SIS™ system are those imposed by the DeltaV architecture: 100 nodes and 30,000 DSTs. With the largest safety systems being about 5,000 I/O, there are no practical limits on the DeltaV SIS system.

Increased Communications Bandwidth and Global Publishers

The number of global parameters has increased and global communications bandwidth on a SISNet ring network has increased by 50%. Users have the flexibility to decide if it makes sense to have a single SISNet ring or multiple SISNet rings. Either way, data can be published to communicate on the same ring or between ring networks.

Integrated, but Separate Fast I/O Updates to DeltaV Controller

Users can reference process variables from logic solver input channels in a DeltaV control algorithm that requires fast I/O



For complete product information, visit
EmersonProcess.com/DeltaVSIS



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